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phase-locked loop or phase lock loop is a
control system that generates an output
signal whose phase is related to the
phase of an input signal. There are
several different types; the simplest is
an electronic circuit consisting of a
variable frequency oscillator and a phase
detector in a feedback loop. The

oscillator generates a periodic signal,
and the phase detector compares the
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loop or PLL is an electronic circuit with a
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"A phase-locked loop or phase lock loop (PLL) is a control system that generates an output signal whose phase is related to the phase of an input signal." It doesn't directly say anything about frequency and phase of input and the output being equal.

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In general, a PLL tries to keep its VCO phase-aligned (and therefore frequency-locked) to the input signal. If you'd like to demodulate a frequency-modulated signal, then you make sure the loop bandwidth (set by the LPF) is wider than the modulating signal, allowing the the VCO to track the incoming frequency, and then the VCO control voltage will be a replica of the original modulating signal.

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The phase locked loop or PLL is an electronic circuit with a voltage controlled oscillator, whose output frequency is continuously adjusted according to the input signal's frequency. A Phase locked loop is used for tracking phase and frequency of the

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