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Yijin Guan et al. FP-DNN: An automated framework for mapping deep neural networks onto FPGAs with RTL-HLS hybrid templates. In Proc. of FCCM, 2017. Google Scholar; Yufei Ma et al. An automatic RTL compiler for high-throughput FPGA implementation of diverse deep convolutional neural networks. In Prof. of FPL, 2017. Google Scholar

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precision choice upon the accuracy of a DNN. Finally, we present a semi-automatic software framework for an automatic precision and accuracy analysis. Our versatile tool has a front-end accepting DNN models from common design frameworks such as Tensorflow/Keras, and is based on a generic error analysis technique, parametrizable by the target

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