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pronounced as
"eighty-
eighty-five"
microprocesso
r. It is an 8-bit
microprocesso
r designed by
Intel in 1977
using NMOS
technology. It
has the
following
configuration
– 8-bit data
bus; 16-bit
address bus,
which can
address upto
64KB; A 16-bit
program
counter; A 16-
bit stack
pointer; Six 8-
bit registers
arranged in
pairs: BC, DE,
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output device
is interfaced
with 8-bit
microprocesso

r 8085A. The
interfacing
circuit is
shown in
figure. The
interfacing
circuit makes
use of 3 Line
to 8 Line
decoder
having 3
enable lines E
1, E⁻ 2, E⁻
3.The address
of the device
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address lines. The memory size of any microprocessor depends on the number of address lines. The general formula is 2^n , where n = number of address lines. For 8085 there are $2^{16}= 64$ Kbytes memory size.Huge Question Bank for Microprocessor 8085 | 8085 ...8085 microprocessor Trainer kit is proposed to smooth the progress of learning and developing designs of 8085 microprocesso

r family from Intel. This kit can be programmed using a PC's 101/104 Keyboard without using a pc, thus making this kit as a standalone kit. User can enter programs in Assembly languages.8085 Lab Trainer Kit- - Pantech SolutionsThe Intel 8085 is an 8-bit microprocessor produced by Intel and introduced in March 1976. It is a software-binary compatible with the more-famous Intel 8080 with only

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wer: (d). (I) , (III) & (IV) 5. Consider the following statements: In 8085 microprocessor, data-bus and address bus are multiplexed in order to. I)Increase the speed of microprocessor. II)Reduce the number of pins. III)Connect more peripheral chips.Microprocessor Multiple choice Questions and Answers-Intel ...The code for 8-bit multiplication for Microprocesso

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bus; 16-bit address bus, which can address upto 64KB; A 16-bit program counter; A 16-bit stack pointer; Six 8-bit registers arranged in pairs: BC, DE, HL
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 An output device is interfaced with 8-bit microprocessor 8085A. The interfacing circuit is shown in figure. The interfacing circuit makes use of 3 Line to 8 Line decoder

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Answer: (d), (I), (III) & (IV)

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