

Arm Cortex M0 Workshop

Information Security and Cryptology – ICISC 2019
 Dependable Computing - EDCC 2020 Workshops
 Teensy LC Development Workshop
 Concepts, Applications, Experimentation and Analysis of Wireless Sensor Networks
 25th International Conference, Munich, Germany, February 28 - March 2, 2012. Proceedings
 5th International Conference on Cryptology and Information Security in Latin America, Havana, Cuba, September 20–22, 2017, Revised Selected Papers
 The Designer's Guide to the Cortex-M Processor Family
 Cryptographic Hardware and Embedded Systems -- CHES 2015
 ICWE 2021 Workshops
 RV 2015, GPID 2013, VG 2015, EO4AS 2015, MCBMIA 2015, and VSWS 2015, Auckland, New Zealand, November 23-27, 2015. Revised Selected Papers
 9th International Workshop, CBCrypto 2021, Munich, Germany, June 21–22, 2021 Revised Selected Papers
 Euro-Par 2017: Parallel Processing Workshops
 8th International Workshop, COSADE 2017, Paris, France, April 13-14, 2017, Revised Selected Papers
 Architecture of Computing Systems - ARCS 2012
 Energy-Efficient Distributed Computing Systems
 ICWE 2021 International Workshops, BECS and Invited Papers, Biarritz, France, May 18–21, 2021, Revised Selected Papers
 IEEE Workshop on Signal Processing Systems
 ISCA 2010 International Workshops A4MMC, AMAS-BT, EAMA, WEED, WIOSCA, Saint-Malo, France, June 19-23, 2010, Revised Selected Papers
 Advances in Theory, Algorithms and Applications
 Euro-Par 2020: Parallel Processing Workshops
 Smart Card Research and Advanced Applications
 Zurich, Switzerland, September 6-7 and 12, 2014, Proceedings, Part III
 A Tutorial Approach
 Raspberry Pi Technology
 Progress in Cryptology – LATINCRYPT 2017
 13th International Workshop on Rapid System Prototyping
 Sensing Systems and Pervasive Intelligence
 Euro-Par 2019: Parallel Processing Workshops
 The Designer's Guide to the Cortex-M Processor Family
 Proceedings, 2nd IEEE and ACM International Workshop on Augmented Reality (IWAR'99)
 Image and Video Technology – PSIVT 2015 Workshops
 SparkFun Simblee BLE Development Workshop
 6th International Conference, SPACE 2016, Hyderabad, India, December 14-18, 2016, Proceedings
 17th International Workshop, Saint-Malo, France, September 13-16, 2015, Proceedings
 Euro-Par 2017 International Workshops, Santiago de Compostela, Spain, August 28-29, 2017, Revised Selected Papers
 12th International Workshop, COSADE 2021, Lugano, Switzerland, October 25–27, 2021, Proceedings
 Security, Privacy, and Applied Cryptography Engineering
 Euro-Par 2018 International Workshops, Turin, Italy, August 27-28, 2018, Revised Selected Papers

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Information Security and Cryptology – ICISC 2019 Springer

This book constitutes revised selected papers from the workshops held at the 26th International Conference on Parallel and Distributed Computing, Euro-Par 2020, which took place in Warsaw, Poland, in August 2020. The workshops were held virtually due to the coronavirus pandemic. The 27 full papers presented in this volume were carefully reviewed and selected from 50 submissions. Euro-Par is an annual, international conference in Europe, covering all aspects of parallel and distributed processing. These range from theory to practice, from small to the largest parallel and distributed systems and infrastructures, from fundamental computational problems to full-edged applications, from architecture, compiler, language and interface design and implementation to tools, support infrastructures, and application performance aspects.

Springer Nature

This book constitutes revised selected papers from the 8th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2017, held in Paris, France, in April 2017. The 17 papers presented in this volume were carefully reviewed and selected from numerous submissions. They were organized in topical sections named: Side-Channel Attacks and Technological Effects; Side-Channel Countermeasures; Algorithmic Aspects in Side-Channel Attacks; Side-Channel Attacks; Fault Attacks; Embedded Security; and Side-Channel Tools.

Dependable Computing - EDCC 2020 Workshops PE Press

This book constitutes revised selected papers from the 22nd International Conference on Information Security and Cryptology, ICISC 2019, held in Seoul, South Korea, in December 2019. The total of 18 papers presented in this volume were carefully reviewed and selected from 43 submissions. The papers were organized in topical sections named: public-key encryption and implementation; homomorphic encryption; secure multiparty computation; post-quantum cryptography; secret sharing and searchable encryption; storage security and information retrieval; and attacks and software security.

Teensy LC Development Workshop Springer

This book constitutes revised selected papers from the 11th International Workshop on Constructive Side-Channel Analysis and Secure Design, COSADE 2021, held in Lugano, Switzerland, in October 2021. The 14 full papers carefully reviewed and selected from 31 submissions are presented in this volume together with the 4 extended keynote abstracts. The workshop covers the following subjects: cryptography, side-channel analysis, cryptographic implementations, fault attacks, implementation attacks, post-quantum cryptography, hardware accelerators, etc.

[Concepts, Applications, Experimentation and Analysis of Wireless Sensor Networks](#) Springer

The energy consumption issue in distributed computing systems raises various monetary, environmental and system performance concerns. Electricity consumption in the US doubled from 2000 to 2005. From a financial and environmental standpoint, reducing the consumption of electricity is important, yet these reforms must not lead to performance degradation of the computing systems. These contradicting constraints create a suite of complex problems that need to be resolved in order to lead to 'greener' distributed computing systems. This book brings together a group of outstanding researchers that investigate the different facets of green and energy efficient distributed computing. Key features: One of the first books of

its kind Features latest research findings on emerging topics by well-known scientists Valuable research for grad students, postdocs, and researchers Research will greatly feed into other technologies and application domains

[25th International Conference, Munich, Germany, February 28 - March 2, 2012. Proceedings](#) Springer Nature

The four-volume set LNCS 8925, 8926, 8927 and 8928 comprises the thoroughly refereed post-workshop proceedings of the Workshops that took place in conjunction with the 13th European Conference on Computer Vision, ECCV 2014, held in Zurich, Switzerland, in September 2014. The 203 workshop papers were carefully reviewed and selected for inclusion in the proceedings. They were presented at workshops with the following themes: where computer vision meets art; computer vision in vehicle technology; spontaneous facial behavior analysis; consumer depth cameras for computer vision; "chalearn" looking at people: pose, recovery, action/interaction, gesture recognition; video event categorization, tagging and retrieval towards big data; computer vision with local binary pattern variants; visual object tracking challenge; computer vision + ontology applies cross-disciplinary technologies; visual perception of affordance and functional visual primitives for scene analysis; graphical models in computer vision; light fields for computer vision; computer vision for road scene understanding and autonomous driving; soft biometrics; transferring and adapting source knowledge in computer vision; surveillance and re-identification; color and photometry in computer vision; assistive computer vision and robotics; computer vision problems in plant phenotyping; and non-rigid shape analysis and deformable image alignment. Additionally, a panel discussion on video segmentation is included.

[5th International Conference on Cryptology and Information Security in Latin America, Havana, Cuba, September 20-22, 2017, Revised Selected Papers](#) IEEE

This book constitutes the thoroughly refereed post-conference proceedings of six international workshops held in the framework of the 7th Pacific-Rim Symposium on Image and Video Technology, PSIVT 2015, during November 23-24, 2015, in Auckland, New Zealand. The 29 revised full papers presented were carefully selected from 58 submissions. Their topics diversely ranged from well-established areas to novel current trends: robot vision, RV 2015; 2D and 3D geometric properties from incomplete data, GPID 2015; vision meets graphics, VG 2015; passive and active electro-optical sensors for aerial and space imaging, EO4AS 2015; mathematical and computational methods in biomedical imaging and image analysis, MCBMIIA 2015; and video surveillance, VSWs 2015.

[The Designer's Guide to the Cortex-M Processor Family](#) PE Press

This book constitutes revised selected papers from the workshops held at 25th International Conference on Parallel and Distributed Computing, Euro-Par 2019, which took place in Göttingen, Germany, in August 2019. The 53 full papers and 10 poster papers presented in this volume were carefully reviewed and selected from 77 submissions. Euro-Par is an annual, international conference in Europe, covering all aspects of parallel and distributed processing. These range from theory to practice, from small to the largest parallel and distributed systems and infrastructures, from fundamental computational problems to full-edged applications, from architecture, compiler, language and interface design and implementation to tools, support infrastructures, and application performance aspects. Chapter "In Situ Visualization of Performance-Related Data in Parallel CFD Applications" is available open access under a Creative Commons Attribution 4.0 International License via link.springer.com.

[Cryptographic Hardware and Embedded Systems -- CHES 2015](#) Springer

The Designer's Guide to the Cortex-M Family is a tutorial-based book giving the key concepts required to develop programs in C with a Cortex M-based processor. The book begins with an overview of the Cortex-M family, giving architectural descriptions supported with practical examples, enabling the engineer to easily develop basic C programs to run on the Cortex-M0/M0+/M3 and M4. It then examines the more advanced features of the Cortex architecture such as memory protection, operating modes and dual stack operation. Once a firm grounding in the Cortex M processor has been established the book introduces the use of a small footprint RTOS and the CMSIS DSP library. With this book you will learn: The key differences between the Cortex M0/M0+/M3 and M4 How to write C programs to run on Cortex-M based processors How to make best use of the Coresight debug system How to do RTOS development The Cortex-M operating modes and memory protection Advanced software techniques that can be used on Cortex-M microcontrollers How to optimise DSP code for the cortex M4 and how to build real time DSP systems An Introduction to the Cortex microcontroller software interface standard (CMSIS), a common framework for all Cortex M-based microcontrollers Coverage of the CMSIS DSP library for Cortex M3 and M4 An evaluation tool chain IDE and debugger which allows the accompanying example projects to be run in simulation on the PC or on low cost hardware

[ICWE 2021 Workshops](#) Springer

This book features the manuscripts accepted for the Special Issue "Applications in Electronics Pervading Industry, Environment and Society—Sensing Systems and Pervasive Intelligence" of the MDPI journal Sensors. Most of the papers come from a selection of the best papers of the 2019 edition of the "Applications in Electronics Pervading Industry, Environment and Society" (APPLEPIES) Conference, which was held in November 2019. All these papers have been significantly enhanced with novel experimental results. The papers give an overview of the trends in research and development activities concerning the pervasive application of electronics in industry, the environment, and society. The focus of these papers is on cyber physical systems (CPS), with research proposals for new sensor acquisition and ADC (analog to digital converter) methods, high-speed communication systems, cybersecurity, big data management, and data processing including emerging machine learning techniques. Physical implementation aspects are discussed as well as the trade-off found between functional performance and hardware/system costs.

[RV 2015, GPID 2013, VG 2015, EO4AS 2015, MCBMIIA 2015, and VSWs 2015, Auckland, New Zealand, November 23-27, 2015. Revised Selected Papers](#) PE Press

This book constitutes the refereed proceedings of seven workshops held at the 18th International Conference on Image Analysis and Processing, ICIAP 2015, in Genoa, Italy, in September 2015: International Workshop on Recent Advances in Digital Security: Biometrics and Forensics, BioFor 2015; International Workshop on Color in Texture and Material Recognition, CTMR 2015; International Workshop on Medical Imaging in Rheumatology: Advanced applications for the analysis of inflammation and damage in the rheumatoid joint, RHEUMA 2015; International Workshop on Image-Based Smart City Application, ISCA 2015; International Workshop on Multimedia Assisted Dietary Management, MADIa 2015; International Workshop on

Scene Background Modeling and Initialization, SBMI 2015; and International Workshop on Image and Video Processing for Quality of Multimedia Experience, QoEM 2015.

[9th International Workshop, CBCrypto 2021, Munich, Germany, June 21-22, 2021 Revised Selected Papers](#) MDPI

This book constitutes the thoroughly refereed post-conference proceedings of the workshops held at the 37th International Symposium on Computer Architecture, ISCA 2010, in Saint-Malo, France, in June 2010. The 28 revised full papers presented were carefully reviewed and selected from the lectures given at 5 of these workshops. The papers address topics ranging from novel memory architectures to emerging application design and performance analysis and encompassed the following workshops: A4MMC, applications for multi- and many-cores; AMAS-BT, 3rd workshop on architectural and micro-architectural support for binary translation; EAMA, the 3rd Workshop for emerging applications and many-core architectures; WEED, 2nd Workshop on energy efficient design, as well as WIOSCA, the annual workshop on the interaction between operating systems and computer architecture.

[Euro-Par 2017: Parallel Processing Workshops](#) Springer Nature

This book is a printed edition of the Special Issue "Raspberry Pi Technology" that was published in Electronics

[8th International Workshop, COSADE 2017, Paris, France, April 13-14, 2017. Revised Selected Papers](#) IEEE Computer Society Press

This book constitutes the refereed proceedings of the First Conference on Cybersecurity of Industrial Control Systems, CyberICS 2015, and the First Workshop on the Security of Cyber Physical Systems, WOS-CPS 2015, held in Vienna, Austria, in September 2015 in conjunction with ESORICS 2015, the 20th annual European Symposium on Research in Computer Security. The 6 revised full papers and 2 short papers of CyberICS 2015 presented together with 3 revised full papers of WOS-CPS 2015 were carefully reviewed and selected from 28 initial submissions. CyberICS 2015 focuses on topics covering ICSS, including cyber protection and cyber defense of SCADA systems, plant control systems, engineering workstations, substation equipment, programmable logic controllers, PLCs, and other industrial control systems. WOS-CPS 2015 deals with the Security of Cyber Physical Systems, that exist everywhere around us, and range in size, complexity and criticality, from embedded systems used in smart vehicles, to SCADA systems in smart grids to control systems in water distribution systems, to smart transportation systems etc.

[Architecture of Computing Systems - ARCS 2012](#) John Wiley & Sons

This book helps you to get started with Teensy LC development. The following is a list of highlight topics in this book. * Preparing Development Environment * Setting Up Teensy LC * Digital I/O Programming * Working with UART * Working with PWM and Analog Input * Working with I2C * Working with SPI * Controlling Teensy LC through Firmata Protocol * Sensing Temperature and Humidity with DHT Module

[Energy-Efficient Distributed Computing Systems](#) Springer

[Arduino Sketch for STM32 Development Workshop](#) PE Press

[ICWE 2021 International Workshops, BECS and Invited Papers, Biarritz, France, May 18-21, 2021. Revised Selected Papers](#) Newnes

The new edition of this popular book has been transformed into a hands-on textbook, focusing on the principles of wireless sensor networks (WSNs), their applications, their protocols and standards, and their analysis and test tools; a meticulous care has been accorded to the definitions and terminology. To make WSNs felt and seen, the adopted technologies as well as their manufacturers are presented in detail. In introductory computer networking books, chapters sequencing follows the bottom up or top down architecture of the seven layers protocol. This book starts some steps later, with chapters ordered based on a topic's significance to the elaboration of wireless sensor networks (WSNs) concepts and issues. With such a depth, this book is intended for a wide audience, it is meant to be a helper and motivator, for both the senior undergraduates, postgraduates, researchers, and practitioners; concepts and WSNs related applications are laid out, research and practical issues are backed by appropriate literature, and new trends are put under focus. For senior undergraduate students, it familiarizes readers with conceptual foundations, applications, and practical project implementations. For graduate students and researchers, transport layer protocols and cross-layering protocols are presented and testbeds and simulators provide a must follow emphasis on the analysis methods and tools for WSNs. For practitioners, besides applications and deployment, the manufacturers and components of WSNs at several platforms and testbeds are fully explored.

[IEEE Workshop on Signal Processing Systems](#) Springer Nature

This book constitutes the refereed proceedings of the 25th International Conference on Architecture of Computing Systems, ARCS 2012, held in Munich, Germany, in February/March 2012. The 20 revised full papers presented in 7 technical sessions were carefully reviewed and selected from 65 submissions. The papers are organized in topical sections on robustness and fault tolerance, power-aware processing, parallel processing, processor cores, optimization, and communication and memory.

[ISCA 2010 International Workshops A4MMC, AMAS-BT, EAMA, WEED, WIOSCA, Saint-Malo, France, June 19-23, 2010. Revised Selected Papers](#) Arduino Sketch for STM32 Development Workshop

This book constitutes the refereed proceedings of the 6th International Conference on Security, Privacy, and Applied Cryptography Engineering, SPACE 2016, held in Hyderabad, India, in December 2016. This annual event is devoted to various aspects of security, privacy, applied cryptography, and cryptographic engineering. This is indeed a very challenging field, requiring the expertise from diverse domains, ranging from mathematics to solid-state circuit design.

[Advances in Theory, Algorithms and Applications](#) Springer

This book constitutes refereed proceedings of the Workshops of the 16th European Dependable Computing Conference, EDCC: Workshop on Artificial Intelligence for Railways, AI4RAILS 2020, Workshop on Dynamic Risk Management for Autonomous Systems, DREAMS 2020, Workshop on Dependable Solutions for Intelligent Electricity Distribution Grids, DSOGR1 2020, Workshop on Software Engineering for Resilient Systems, SERENE 2020, held in September 2020. Due to the COVID-19 pandemic the workshops were held virtually. The 12 full papers and 4 short papers were thoroughly reviewed and selected from 35 submissions. The workshop papers complement the main conference topics by addressing dependability or security issues in specific application domains or by focussing in specialized topics, such as system resilience.

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