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Click "add IP" and enter "zynq" into the search field. Select the "ZYNQ7 Processing System". IP blocks can later be added by right clicking in the diagram area and selecting "Add IP" in the menu that pops up. The diagram view should now contain a Zynq processing system as shown in the figure below.

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Added Chapter 3, Getting Started with the Base Targeted Reference Design. Added Chapter 4, Using the AMS101 Evaluation Card. Additional references were added through the book and to Appendix A, Additional Resources. 09/18/12 2.0 The ZC702 Evaluation kit now includes a USB Micro-B to female A adapter. Added *Zedboard*

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Zynq Evaluation Kit Getting Started Guide

This example shows how generate and run code from a Simulink® model onto a Xilinx Zynq ZC702 evaluation kit with a VxWorks® 7 operating system.

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